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| TRANSMITTAL FORM (to be used for all correspondence after initial filing) | Application Number | 10/776,483 | |
| | Filing Date | February 10, 2004 | |
| | First Named Inventor | Sohrab Kianian et al. | |
| | Art Unit | 2818 | |
| | Examiner Name | Not assigned yet | |
| Total Number of Pages in This Submission | 6 | Attorney Docket Number | 2102397-992012 |

| ENCLOSURES (Check all that apply) | | |
|---|--|---|
| <input type="checkbox"/> Fee Transmittal Form <input type="checkbox"/> Fee Attached <input type="checkbox"/> Amendment/Reply <input type="checkbox"/> After Final <input type="checkbox"/> Affidavits/declaration(s) <input type="checkbox"/> Extension of Time Request <input type="checkbox"/> Express Abandonment Request <input type="checkbox"/> Information Disclosure Statement <input type="checkbox"/> Certified Copy of Priority Document(s) <input type="checkbox"/> Response to Missing Parts/Incomplete Application <input type="checkbox"/> Response to Missing Parts under 37 CFR 1.52 or 1.53 | <input type="checkbox"/> Drawing(s) <input type="checkbox"/> Licensing-related Papers <input type="checkbox"/> Petition <input type="checkbox"/> Petition to Convert to a Provisional Application <input type="checkbox"/> Power of Attorney, Revocation Change of Correspondence Address <input type="checkbox"/> Terminal Disclaimer <input type="checkbox"/> Request for Refund <input type="checkbox"/> CD, Number of CD(s) _____ | <input type="checkbox"/> After Allowance communication to Technology Center (TC) <input type="checkbox"/> Appeal Communication to Board of Appeals and Interferences <input type="checkbox"/> Appeal Communication to Group (Appeal Notice, Brief, Reply Brief) <input type="checkbox"/> Proprietary Information <input type="checkbox"/> Status Letter <input checked="" type="checkbox"/> Other Enclosure(s) (please identify below): 1. Request For Correction Of The Official Filing Receipt (in duplicate); 2. Copy of the Official Filing Receipt with corrections in red; 3. Return Postcard |
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| SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT | |
|--|--|
| Firm Or Individual name | Alan A. Limbach, Reg. No. 39,749 Gray Cary Ware & Freidenrich LLP |
| Signature | |
| Date | May 19, 2004 |

| CERTIFICATE OF TRANSMISSION/MAILING | |
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| Typed or printed name | Kathleen LaBrie |
| Signature | |
| Date | May 19, 2004 |

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Attorney Docket No.: 2102397-992012

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Sohrab Kianian et al.

Examiner: Not assigned

Serial No: 10/776,483

Art Unit: 2818

Filed: February 10, 2004

**REQUEST FOR CORRECTION OF
THE OFFICIAL FILING RECEIPT**

Title: **METHOD OF OPERATING A SEMICONDUCTOR MEMORY ARRAY OF FLOATING GATE MEMORY CELLS WITH BURIED BIT-LINE AND VERTICAL WORD LINE TRANSISTOR**

Office of Initial Patent Examination's
Filing Receipt Corrections
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Attached is a copy of the Official Filing Receipt which was received in our office on May 14, 2004 in connection with the above-identified patent application.

The Official Filing Receipt is in error. Please amend the filing receipt by correcting the Title as follows: Method Of Operating A Semiconductor Memory Array Of Floating Gate Memory Cells With Buried Bit-Line And Vertical **Word Line Transistor**.

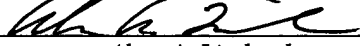
The Commissioner is hereby authorized to charge any additional fees which may be required, or credit any overpayment to Deposit Account No. 07-1896. A duplicate copy of this sheet is enclosed.

Attached is a copy of the Filing Receipt with the requested correction noted in red ink..

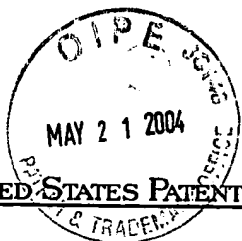
Respectfully submitted,

GRAY CARY WARE & FREIDENRICH LLP

Dated: May 19, 2004

By 
Alan A. Limbach
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| APPL NO. | FILING OR 371 (c) DATE | ART UNIT | FIL FEE REC'D | ATTY. DOCKET NO | DRAWINGS | TOT CLMS | IND CLMS |
|------------|---------------------------|----------|---------------|-----------------|----------|----------|----------|
| 10/776,483 | 02/10/2004 | 2818 | 770 | 2102397-992012 | 19 | 9 | 2 |

26379
GRAY CARY WARE & FREIDENRICH LLP
2000 UNIVERSITY AVENUE
E. PALO ALTO, CA 94303-2248

CONFIRMATION NO. 3244

FILING RECEIPT



OC000000012584386

Date Mailed: 05/11/2004

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

Sohrab Kianian, Los Altos, CA;
Chih Hsin Wang, San Jose, CA;

Domestic Priority data as claimed by applicant

This application is a DIV of 09/982,413 10/17/2001

Foreign Applications

If Required, Foreign Filing License Granted: 05/10/2004

Projected Publication Date: 08/19/2004

Non-Publication Request: No

Early Publication Request: No

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MAY 14 2004

GRAY CARY
WARE & FREIDENRICH

Title

Method of operating a semiconductor memory array of floating gate memory cells with buried bit-line and vertical-world line transistor

WORD TRANSISTOR

Preliminary Class

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Title 35, United States Code, Section 184
Title 37, Code of Federal Regulations, 5.11 & 5.15**

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